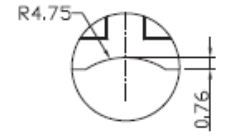
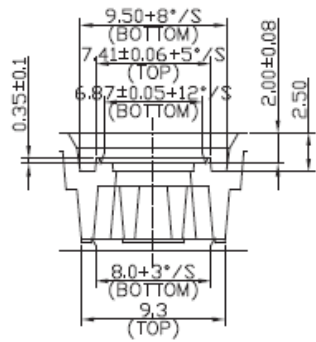
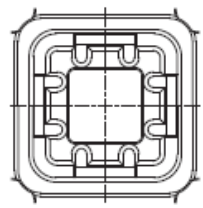
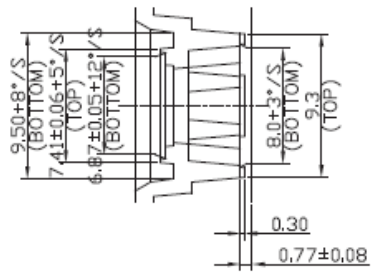
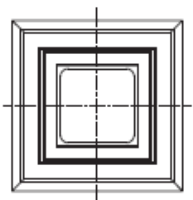
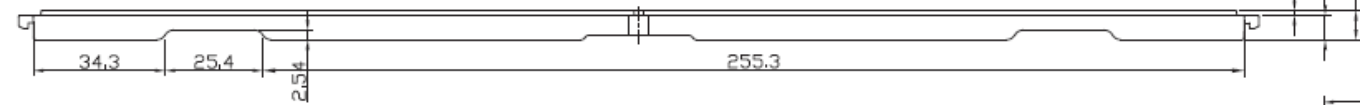


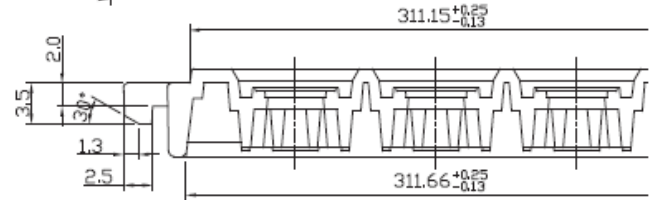
- NOTES
1. SURFACE ELECTRIC RESISTIVITY:
 $\Delta \leq 1.0 \times 10^5$ & $\square \leq 1.0 \times 10^6$ ohm/sq
 2. WARPAGE IS WITHIN 0.76mm
 3. THE CELLS MARKED WITH CROSS ARE FOR VACUUM PICKUP AREA
 4. TOTAL USABLE CELLS $10 \times 25 = 250$



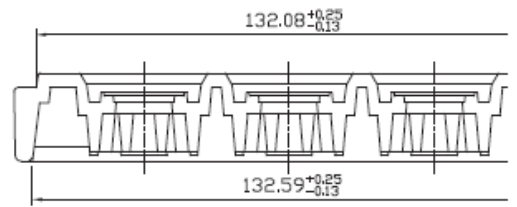
DETAIL 'C' (4:1)



SCALE (4:1)



SECTION 'A' (3:1)



SECTION 'B' (3:1)

DESIGNER 張惠晶 5/20 '10	 ASE ADVANCED SEMICONDUCTOR TECHNOLOGIES, INC.	TITLE (ASE-CL) LQFP 7X7X1.4mm			
CHECKED 蔡清心 5/20 '10		TOLERANCE X±0.25 XX±0.13 X≠1"	THIRD ANGLE PROJECTION 	DRAWING NO. JT2070701	REV. A
APPROVED 劉智光 5/20 '10		SCALE #/* mm 1/2	SHEET NO. 1/2	ASE TRAY DRAWING VERSION : 2	