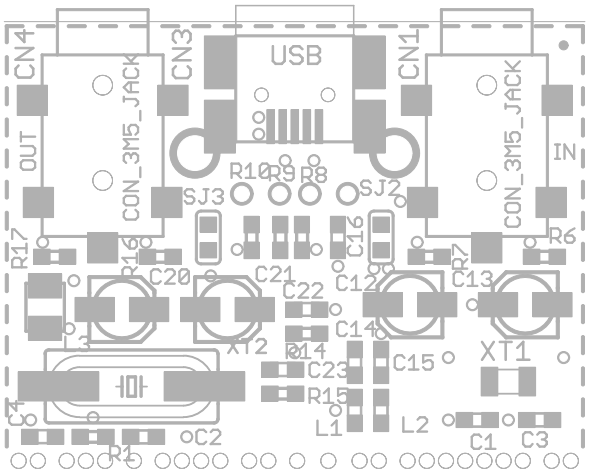
VS1005 Breakout board v.1.1

TOP VIEW



GND	88	87	GND		
GND	86	85	GND		
PWM	84	83	USBP		
USBN	82	81	LEFT		
AVDD1	80	79	NC		
CBUF	78	77	AVDD0		
RIGHT	76	75	AGND		
RCAP	74	73	RF_N		
RF_P	72	71	MIC1N / LINE1_1		
AVDDRF	70	69	MIC2N / AUX4 / LINE3_1		
MIC1P / LINE1_2	68	67	AUX2 / LINE1_3		
MIC2P / AUX3 / LINE3_2			AUX0 / LINE2_2		
AUX1 / LINE2_1					

Stereo line in : LINE1_1 + LINE1_2

Alternate stereo line in : LINE3_1 + LINE3_2

Alternate stereo line in : LINE2_1 + LINE2_2

XTALO	1	2	XTALI		
AVDD	3	4	VHIGH		
CVDD	5	6	IO2VDD		
IOVDD	7	8	XRESET		
NFDIO0 / GPIO0_0	9	10	NFDIO1 / GPIO0_1		
NFDIO2 / GPIO0_2	11	12	NFDIO3 / GPIO0_3		
NFDIO4 / GPIO0_4	13	14	NFDIO5 / GPIO0_5		
NFDIO6 / GPIO0_6	15	16	NFDIO7 / GPIO0_7		
NFRDY / GPIO0_8	17	18	NFRD / GPIO0_9		
XCS1 / GPIO1_4	19	20	SCLK1 / GPIO1_5		
CVDD1	21	22	MISO1 / GPIO1_6		
GND			GND		
GND			GND		

GND			GND		
GND			GND		
XTALI_RTC	66	65	XTALO_RTC		
RTCVDD	64	63	PWRBTN		
TEST	62	61	DBG		
CVDD3	60	59	US_OUT / GPIO0_15		
US_IN / GPIO0_14	58	57	SPDIF_OUT / GPIO0_13		
SPDIF_IN / GPIO0_12	56	55	ETH_TXN / GPIO2_13	/ DIA2	
IOVDD3	54	53	ETH_TXP / GPIO2_12	/ DIA1	
EDC / GPIO1_15	52	51	ETH_RXP / GPIO2_11	/ DIA3	
SD_CMD / GPIO2_10	50	49	SD_DAT3 / GPIO2_9		
SD_DAT2 / GPIO2_8	48	47	SD_DAT1 / GPIO2_7		
SD_DAT0 / GPOI2_6	46	45	SD_CLK / GPIO2_5		

NFWR	24	23	MOSI1 / GPIO1_7		
NFCE / GPIO0_11	26	25	IOVDD1		
SCLK0 / GPIO1_1	28	27	XCS0 / GPIO1_0		
MOSI0 / GPIO1_3	30	29	MISO0 / GPIO1_2		
TDI / GPIO2_1	32	31	TMS / GPIO2_0		
TCK / GPIO2_3	34	33	TDO / GPIO2_2		
CVDD2	36	35	DBGREQ / GPIO2_4		
TX / GPIO1_9	38	37	RX / GPIO1_8		
I2S_DO / GPIO1_11	40	39	I2S_DI / GPIO1_10		
I2S_FRM / GPIO1_13	42	41	I2S_BCK / GPIO1_12		
I2S_12M / GPIO1_14	44	43	IOVDD2		
GND			GND		
GND			GND		

/ GPIO0_10 / DRO / DLO

DIA1 - Digital AD1 input

DIA2 - Digital AD2 input

DIA3 - Digital AD3 input

EDC - External AD/DA clk

DRO - Digital Right output

DLO - Digital Left output