VS23S010 application connection

VSXXXX
- SCLK
- MOSI
- MISO
- XCS1

VS23S010
- SCLK
- MOSI
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SPI flash
- SCLK
- MOSI
- MISO
- XCS1

XCS2
VS23S010 adds capacity of writable memory

External memory is addressed by SPI commands. It is not visible as internal memory of VSXXXX.

Controlled by firmware

Read only, non-volatile

Read/write, volatile

SPI interface

SPI flash
VS23S010 does not increase overlay capacity

Overlay Technique:
Code is dynamically copied from flash memory to IRAM and then executed