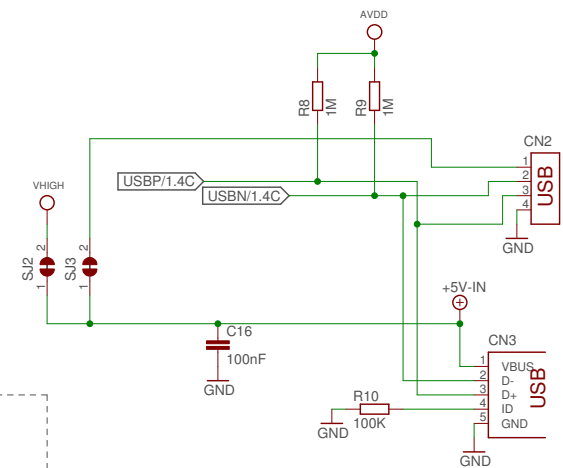
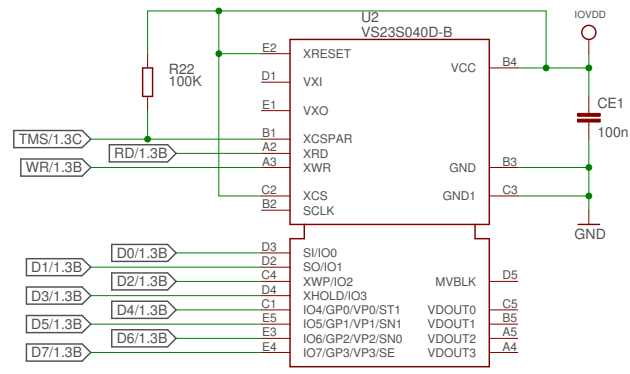


V.1.1  
 - RTC crystal changed to SMD type  
 - USB wiring optimized  
 - GND plane optimized  
 - RS232 Markings on silkscreen

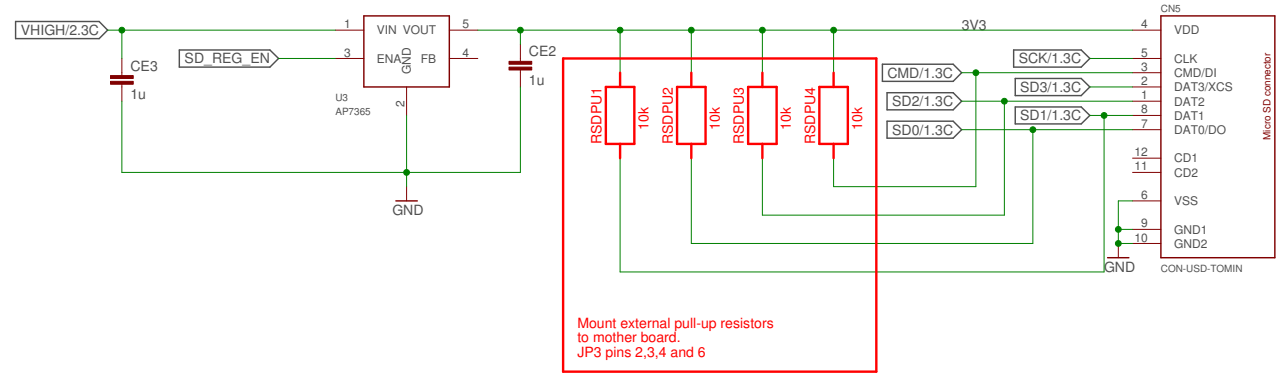


TP5 TP6 TP4  
 TP1 TP2 TP3  
 pcb fiducials

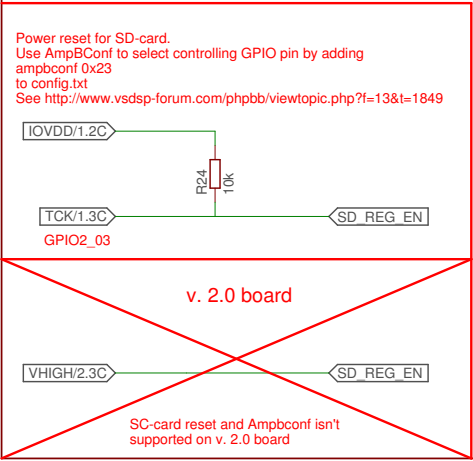
ANALOG I/O		
TITLE: vs1005bob21		
Document Number:	1	REV: 2.1
Date: 16.4.2019 13.13	Sheet: 2/3	



VS23S040  
 - Mounted on bottom side  
 - Usable as memory only  
 - Accessible only with byte-wide bus  
 - Routed on bottom and inside PCB



Mount external pull-up resistors to mother board.  
 JP3 pins 2,3,4 and 6



V.2.0  
 - Added VS23S040  
 - Added SD card connector and regulator  
 - C2, C4 30pF -> 12pF  
 - R23 for separation of line out ground and RF in  
 - R21 not assembled. IOVDD and FVDD separated V2.1  
 - External SD-card pull-ups  
 - SD-card power reset connection

VS23S040 and SD-card	
TITLE: vs1005bob21	
Document Number:	REV: v.2.1
Date: 16.4.2019 13.13	Sheet: 3/3