VS10XX application considerations

Applies to VS1011, VS1002, VS1003, VS1033 and VS1083

Pull-down RESET pin to GND and connect to uC SPI0 pin

Connect TEST pin to I/O000

Connect RX pin to I/O000

Resistors prevent overload during short circuit

Pull down all uC SPI1 pins

Notice: SPI0 controls SPI lanes

Single Linear Regulator Power Example

For best performance use separate regulator for each uIC

Check datasheets for voltage specifications

No inductors for I/O000, I/O00 and I/O003

One 100nF ceramic cap near each I/O pin

Place very close to the input pins

If line input not used:

LINEAR pins can be left floating

ESD protection recommended for the line input

Recommended additional ESD protection

If MIC not used:

MIF and MCF can be left floating

Use either as mic or line input

If MIC is located off board:

ESD protection should be used

Revision history:

Rev 3A
- Updated ESD protection
- Filter caps for line input
- R13 & R14

Rev 3B
- Updated R15

Rev 4A
- Updated for VS1083

See http://www.vlsifl/appnotes for more details