

VS1003 AppNote: ADC power down reduces power consumption

Description

This document describes how to reduce the power consumption of VS1003 by turning off the ADC modulator.

This document applies to VS1003.

Revision History			
Rev	Date	Author	Description
1.00	2006-02-27	PLe	Initial revision.

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1 Turning off ADC modulator

By default the ADC modulator of VS1003 is enabled. When not needed the ADC can be turned off to reduce power consumption.

This is done by setting the AD_DIV register to zero. To do this write 0xC01E to SCI_WRAMADDR and then write 0x0000 to SCI_WRAM.

The following pseudo code shows how to set the AD_DIV register through SCI_WRAMADDR and SCI_WRAM.

```
10 /* Set AD_DIV to zero */  
20 WriteRegister(SCI_WRAMADDR, 0xC01E) /* register to write */  
30 WriteRegister(SCI_WRAM, 0x0000) /* data to write */
```

2 Document Version Changes

This chapter describes the most important changes to this document.

Version 1.00, 2006-02-17

- Initial version.

3 Contact Information

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Note: If you have questions, first see
<http://www.vlsi.fi/vs1001/faq/>
<http://www.vlsi.fi/vs1011/faq/>
<http://www.vlsi.fi/vs1002/faq/>
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