

VS1011 TO VS1011A MIGRATION CHECKLIST

CHANGE HISTORY VS1011 to VS1011A

Direction of IO pins

- All IOs (GPIO0, GPIO1, SDATA, DCLK) direction masked as input when XRESET pin is low.

ESD improvement

- added internal series resistor to RCAP pin.

Power on reset

- automatic POR generation and initialization of control registers when DVDD rises. Pulse length 500-800 us.

IO pins

- crossed GPIO2/GPIO3 as output pin wiring bug fixed.

REQUIRED CONSIDERATIONS

- GPIO pins should be pull-up or pull-down in reset mode.
- POR keeps the device in reset mode up to 800 us when DVDD rises.
- Check application specific firmware regarding GPIO2 and GPIO3 pins. When they were used as outputs their pins were earlier crossed.
- VS1011A is the first production version (Chartered as wafer foundry).